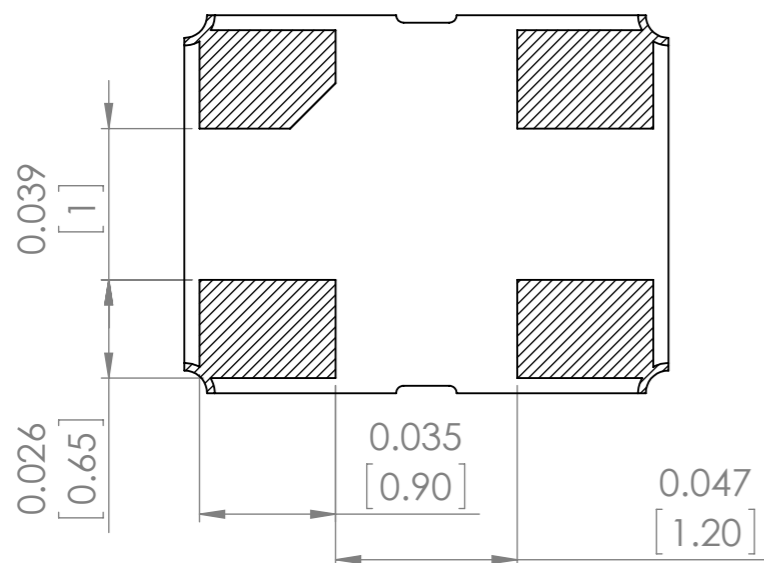



PIN	FUNCTION
1	Tri-state
2	GND/Case
3	Output
4	Vdd



BOTTOM VIEW

Note:
 1. The outline package configuration varies with frequency range. Electrical properties, pin configuration, and land pattern are the same.
 2. Recommend using an approximately 0.01 μF bypass capacitor between Pin 2 and 4.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:	DEBUR AND BREAK SHARP EDGES	DO NOT SCALE DRAWING	REVISION
DRAWN	NAME	SIGNATURE	DATE			 30332 Esperanza, Rancho Santa margarita, California 92688	
CHK'D	SAAZVAT					TITLE: Oscillator	
APPV'D	XXXXXX					DWG NO. ASE2	
MFG						SCALE:20:1	
Q-A					MATERIAL:	SHEET 1 OF 1	
					WEIGHT:	A3	