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LatticeXP Advanced Evaluation Board

The LatticeXP Advanced Evaluation Board provides a stable yet flexible platform designed to help the user quickly evaluate the performance of the LatticeXP FPGA or aid in development of custom designs. For more basic evaluation or development, see the [LatticeXP Standard Evaluation Board](#).



[click for enlarged view](#)

Product Contents

Each LatticeXP Advanced Evaluation Board includes the following:

LatticeXP Advanced Evaluation Board featuring

- LFXP10C-4F388C FPGA Device
- DDR memory interface socket
- 10/100/1G E-net MAC interface
- PCI interface
- On-board FCRAM
- On-board oscillator
- Various LEDs, SMA connections for external clocking, and on-board power control

ispDOWNLOAD cable

Wall Adapter Power Supply (Universal voltage input, US plugs)

Device Support

You will need to following software to use this board:

[ispLEVER](#) for design, fitting, place & route of Lattice programmable devices

[ispVM](#) to download your program to the LatticeXP device

Ethernet MAC Demo

Click [here](#) for more information on a demo utilizing this board and the Lattice Tri-speed Ethernet MAC IP core.

Ordering Information

LFXP10C-H-EV

To purchase, visit our [online store](#).

Contact your [local Lattice sales representative](#) for further information.