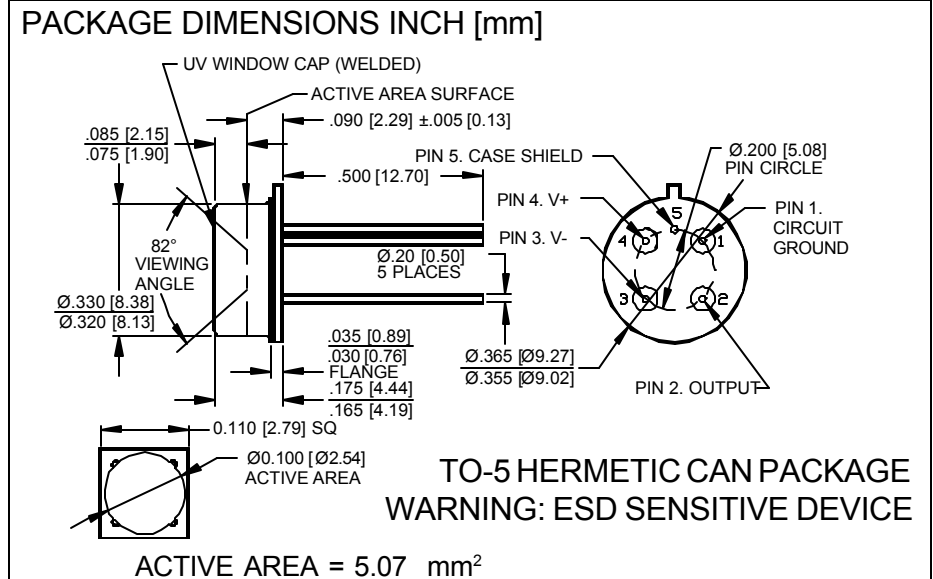


PHOTONIC DETECTORS INC.

Detector Amplifier Hybrid, U.V. Enhanced Type PDU-716-100



FEATURES

- 10 KHz bandwidth
- Internal 100 MOhm gain
- Low offset voltage
- Low input bias current

DESCRIPTION:

The PDU-716-100 is a low noise, medium speed, U.V. enhanced silicon photodiode integrated with a low noise JFET monolithic transimpedance op-amp. There is an internal 100 MOhm feedback gain resistor which limits the bandwidth to 10KHz.

APPLICATIONS

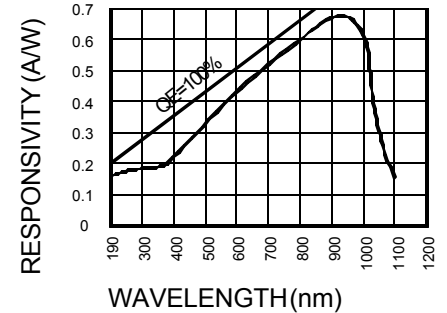
- Medical diagnostic
- Low signal applications
- Color analysis
- Analytical chemistry

ABSOLUTE MAXIMUM RATING (TA=25°C unless otherwise noted)

SYMBOL	PARAMETER	MIN	MAX	UNITS
V _{BR}	Reverse Voltage		15	V
T _{STG}	Storage Temperature	-55	+125	°C
To	Operating Temperature Range	0	+70	°C
T _S	Soldering Temperature*		+240	°C
I _L	Light Current		500	mA

*1/16 inch from case for 3 secs max

SPECTRAL RESPONSE



PHOTODIODE ELECTRO-OPTICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{SC}	Short Circuit Current	H = 100 fc, 2850 K	45	65		μA
I _D	Dark Current	H = 0, V _R = 10 V		1.0	5.0	nA
R _{SH}	Shunt Resistance	H = 0, V _R = 10 mV	.5	2		GΩ
TC R _{SH}	RSH Temp. Coefficient	H = 0, V _R = 10 mV		-8		% / °C
C _J	Junction Capacitance	H = 0, V _R = 10 V**		15		pF
λ _{range}	Spectral Application Range	Spot Scan	190		1100	nm
R	Responsivity	V _R = 0 V, λ = 254 nm	1.2	1.8		A/W
V _{BR}	Breakdown Voltage	I = 10 μA	5	10		V
NEP	Noise Equivalent Power	V _R = 10 V @ Peak		2.5x10 ⁻¹⁴		W/√Hz
tr	Response Time	R _L = 1 KΩ V _R = 10 V		15		nS

Information in this technical data sheet is believed to be correct and reliable. However, no responsibility is assumed for possible inaccuracies or omission. Specifications are subject to change without notice. **f = 1 MHz

AMPLIFIER SPECIFICATION TA = 25° C and VS = ± 15 vdc UNLESS OTHERWISE NOTED

CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
FEEDBACK NETWORK 100 MEGΩ RESISTOR, 1 pF CAPACITOR*	THINFILM RESISTOR TRIMMED TO ±5% *TOL ±5%		100		MEG Ω
V _{IO} INPUT OFFSET VOLTAGE	INITIAL OFFSET FULL RANGE		0.6	3.9	mV
	LONG TERM OFFSET STABILITY		.04		mV/MONTH
I _{IB} INPUT BIAS CURRENT	OFFSET CURRENT, V _{CM} =0		4		pA
R _i INPUT RESISTANCE	DIFFERENTIAL		1 X 10 ⁻¹²		Ω
	COMMONMODE		1 X 10 ⁻¹²		
V _{ICR} INPUT VOLTAGE RANGE	COMMONMODE	-12	+16		V
CMRR COMMON MODE REJECTION RATIO	COMMONMODE REJECTION, R _S = 50 Ω	72	90		dB
V _{N(PP)} INPUT VOLTAGE NOISE	VOLTAGE 0, f=1 KHz		2		μV _{PP}
	VOLTAGE 0, f=10 KHz		40		nV/√Hz
I _N INPUT CURRENT NOISE	f=1 KHz		1		fA / √Hz
B _{OM} FREQUENCY RESPONSE	UNITY GAIN, SMALL SIGNAL R _L = 10 KΩ C _L = 100 pF		2		MHz
	SLEW RATE, UNITY GAIN	2.6	3.4		V/μs
A _{VD} OPEN LOOP GAIN	v _o = ±10 V, R _L = 10 KΩ	20	230		V/mV
V _{OM±} OUTPUT CHARACTERISTICS	VOLTAGE @ R _L = 10 KΩ	±13.2	±13.7		V
	VOLTAGE @ R _L = 600 Ω	±12.5	±13		V
V _{CC±} POWER SUPPLY	OPERATING RANGE	±3.5	±15	±18	V

AMPLIFIER ABSOLUTE MAXIMUM RATING (TA=25°C UNLESS OTHERWISE NOTED)

PARAMETER	MIN	MAX	UNITS
SUPPLY VOLTAGE	±4.5	±18	V
INTERNAL POWER DISSIPATION		500	mW
STORAGE TEMPERATURE	-55	+150	° C
OPERATING TEMPERATURE	0	+70	° C

WARNING: ESD SENSITIVE DEVICE

