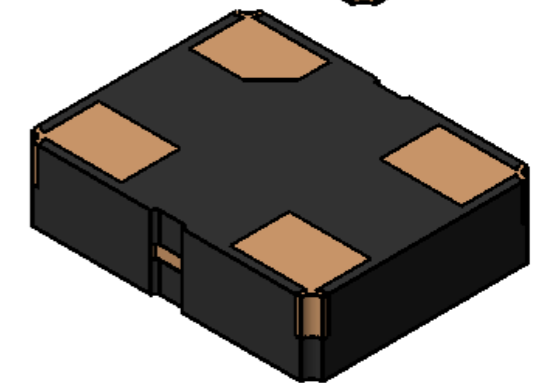
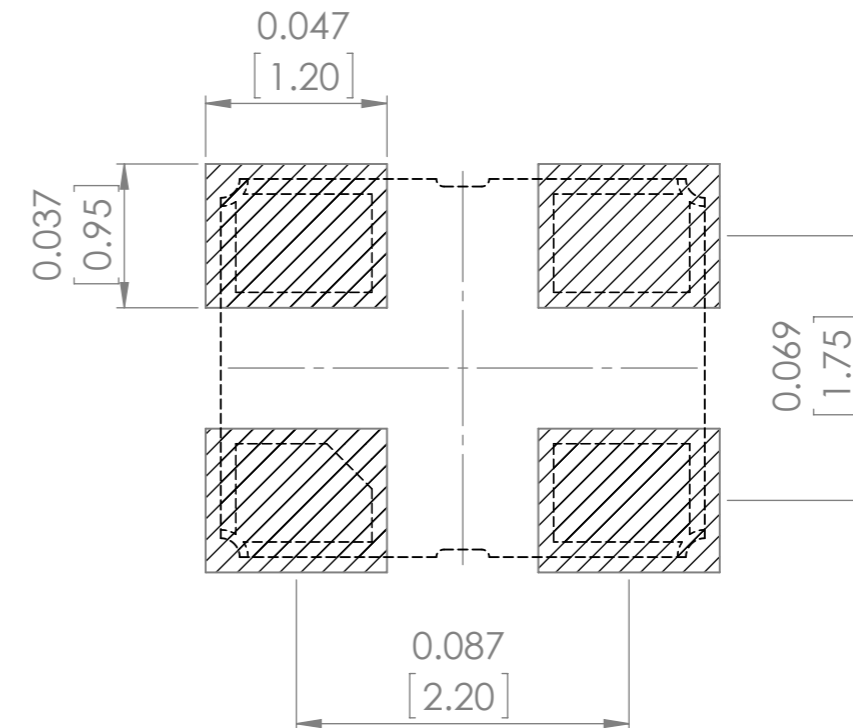
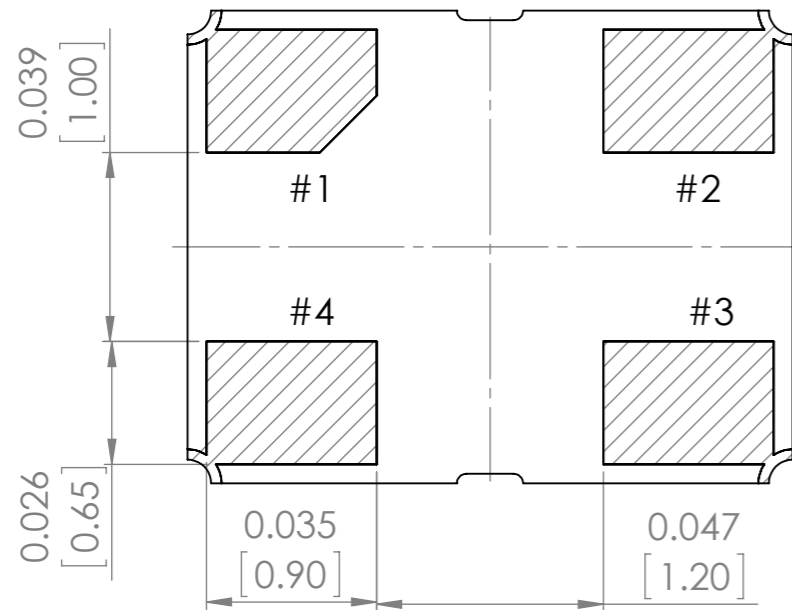


Recommended Land Pattern



Due to material availability, 1 or 2 side pads may appear on the side of the package. The variation in no way affects the electrical performance of the product.



PIN	FUNCTION	
	ASVTX-11	ASTX-11
1	Vc	GND
2	GND	
3	OUTPUT	
4	Vdd	

Note 1: It is not recommended that paths go underneath the oscillator. If needed, please use multi-layer PCB board or resist coating.

Note 2: Recommended using an approximately 0.01 μF bypass capacitor between Pin 2 and 4.

DO NOT SCALE DRAWING	REVISION	-
Apracon LLC, 2 Faraday, Suite#B Irvine, Ca. 92618		
TITLE:	-	
DWG NO.	AS(V)TX-11	A3
SCALE: 25:1	SHEET 1 OF 1	